

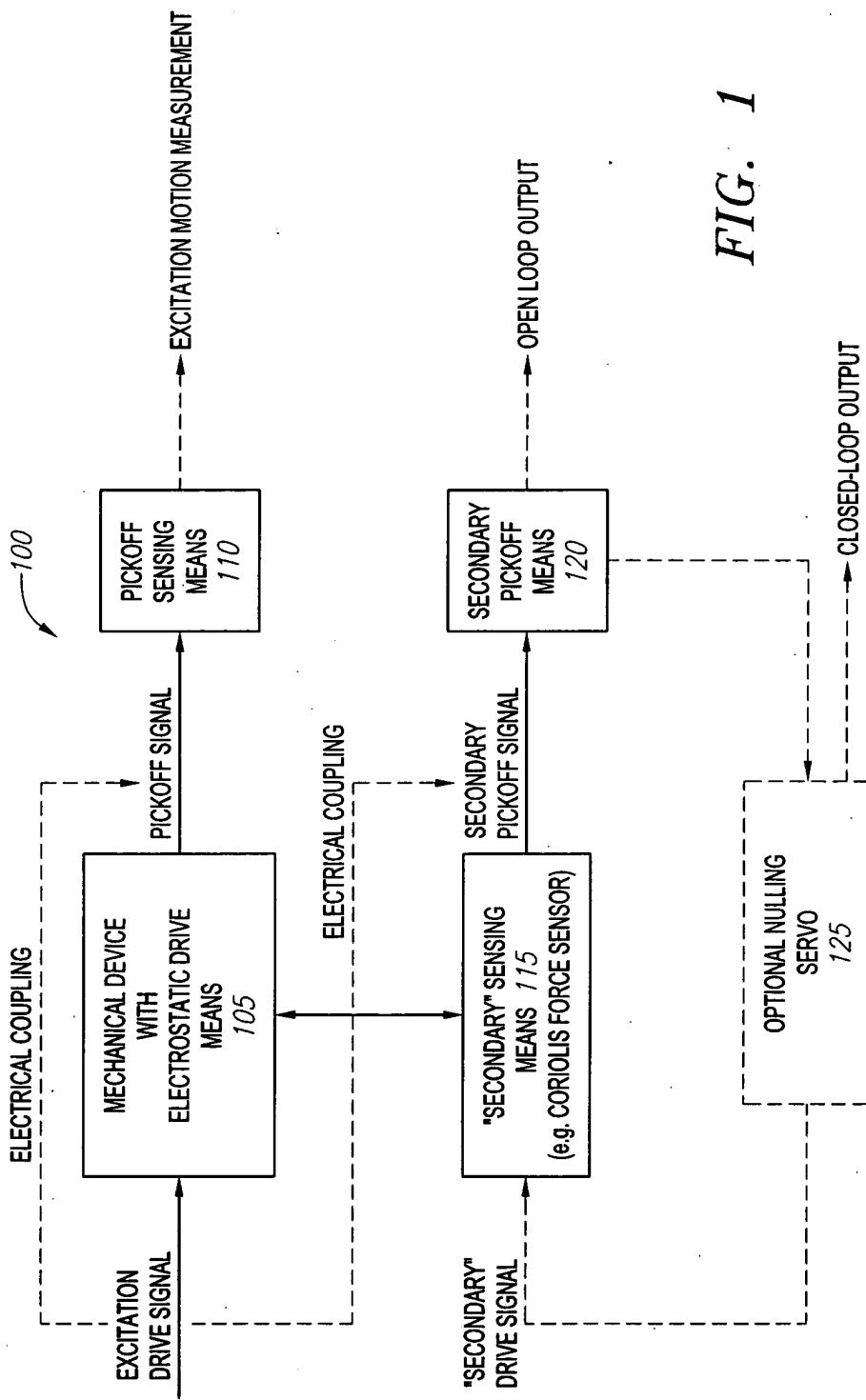
MODULATION METHOD FOR SIGNAL CROSSTALK MITIGATION

IN ELECTROSTATICALLY DRIVEN DEVICES

In re application of: Daniel A. Tazartes et al. - Dkt. No.: 34261-8400

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BLOCK DIAGRAM OF EXCITATION & SENSING



*FIG. 1*

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BLOCK DIAGRAM OF EXCITATION & SENSING WITH POLARITY RANDOMIZATION

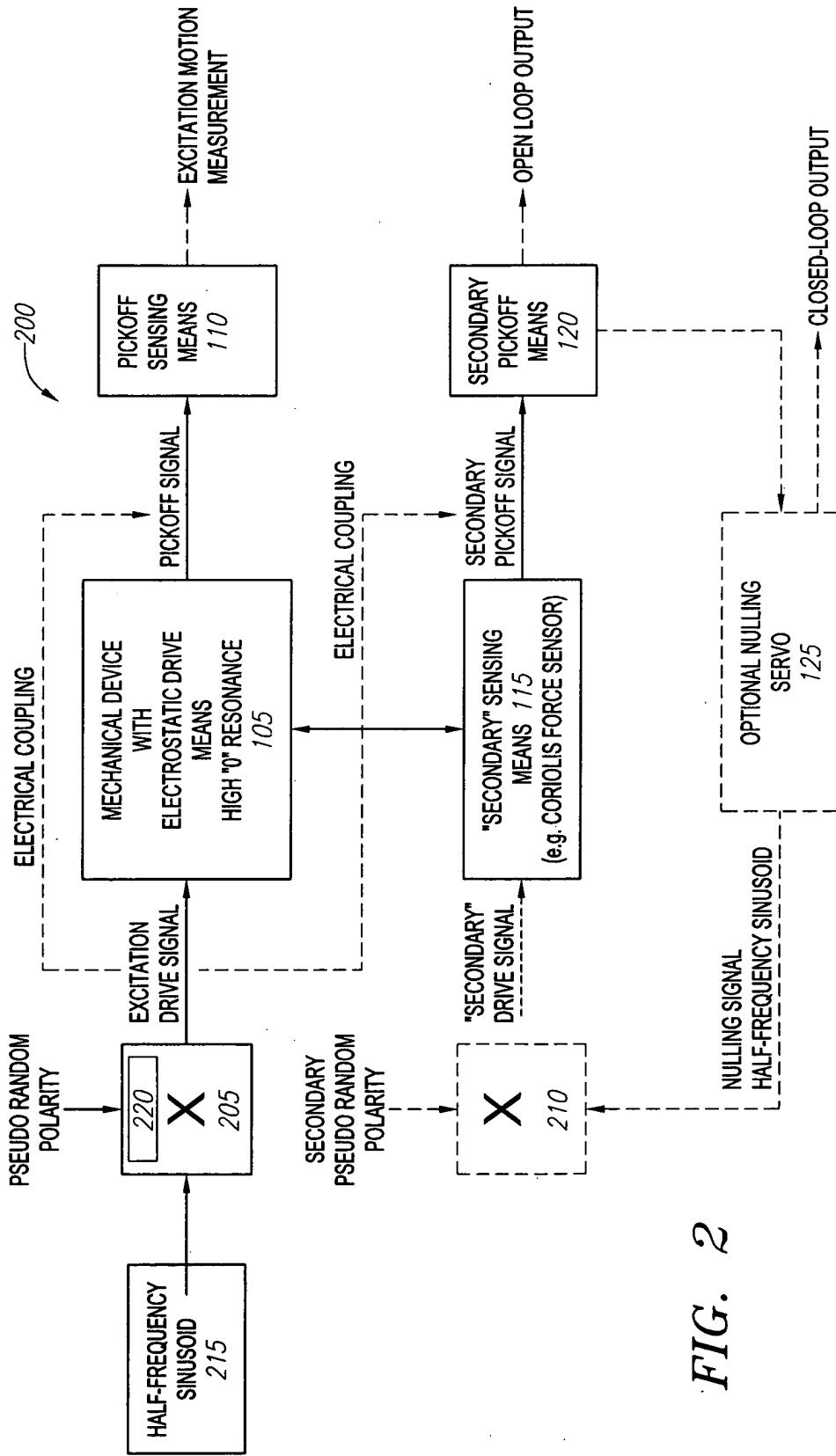


FIG. 2

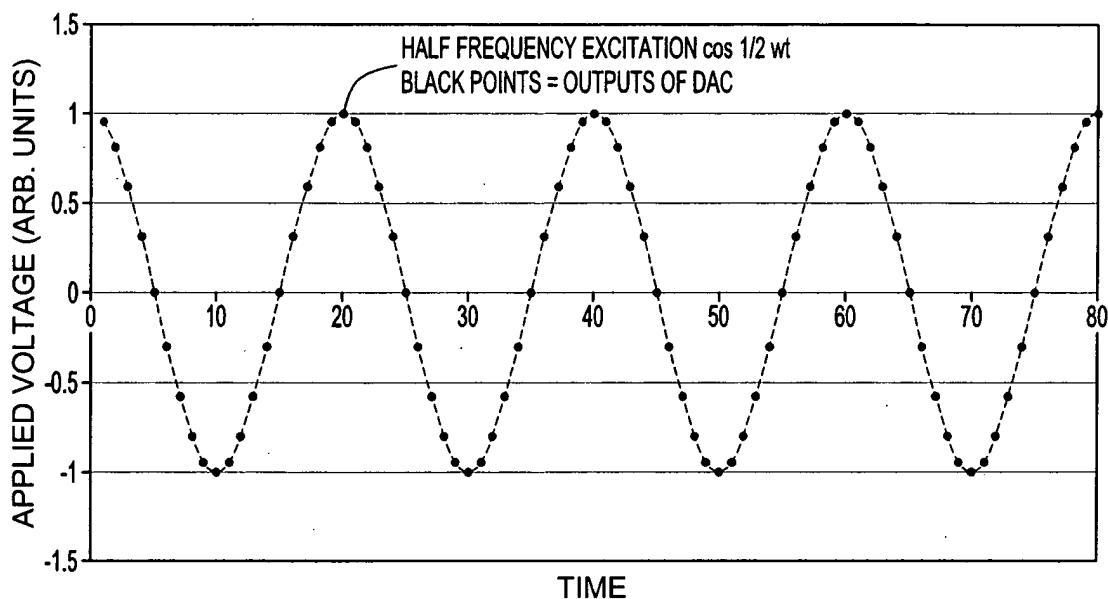
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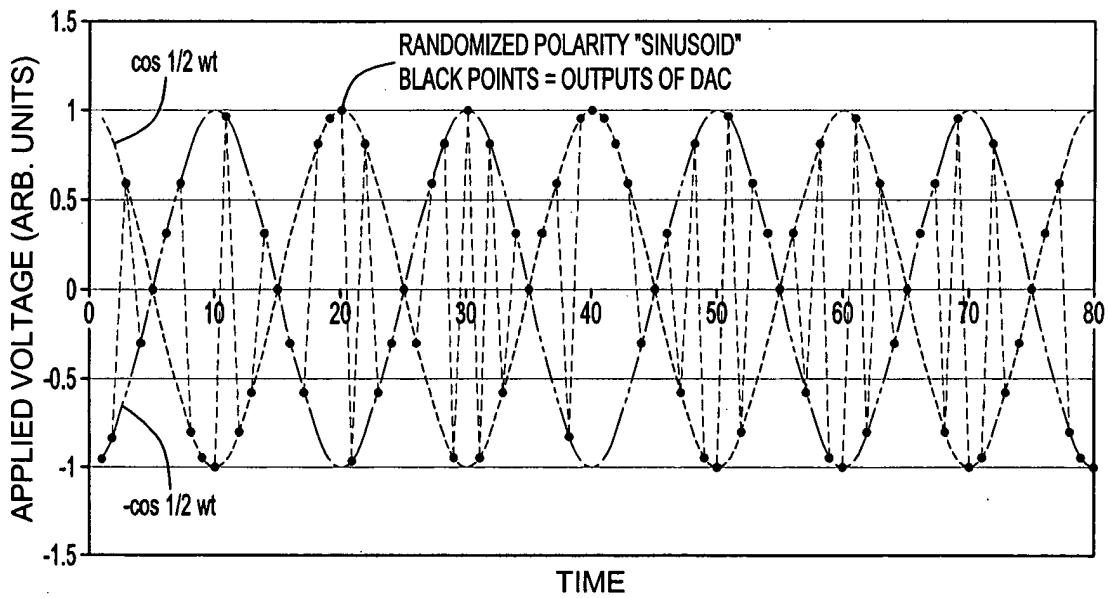
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STANDARD HALF FREQUENCY MODULATION



*FIG. 3*

HALF FREQUENCY MODULATION WITH POLARITY RANDOMIZATION



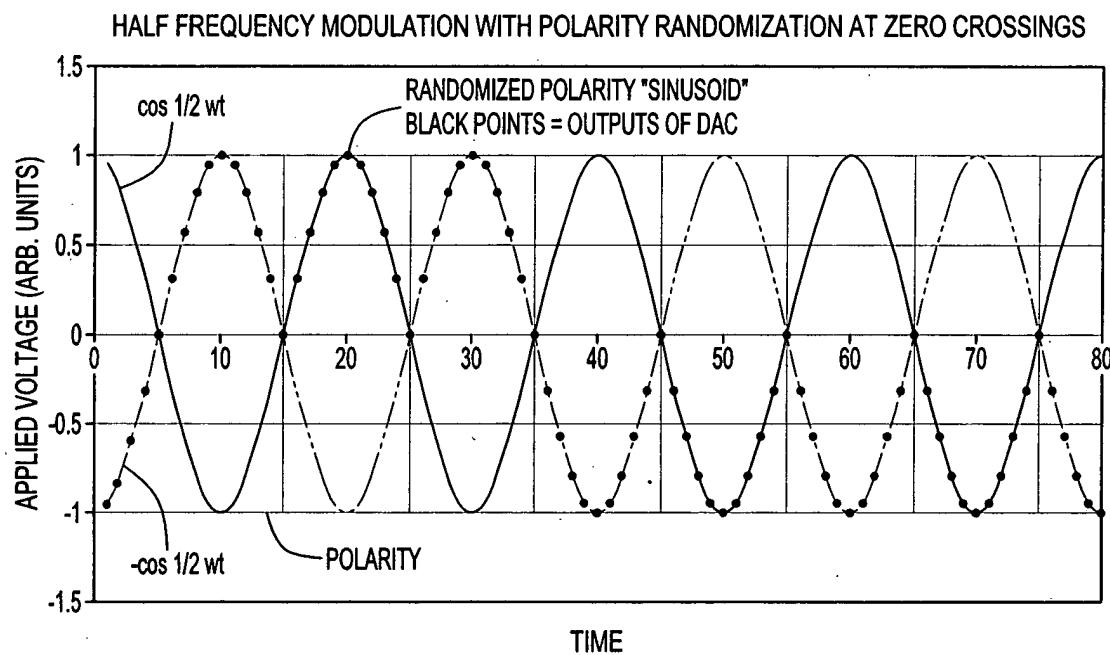
*FIG. 4*

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*FIG. 5*